InP/InGaAs Uni-Traveling-Carrier Photodiode (UTC-PD) with Improved EM Field Response

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Abstract- In this paper a unique structure of InP/InGaAs Uni-Traveling-Carrier Photodiode (UTC-PD) is proposed. Compared with the one-sided junction photodiode, the UTC-PD has the advantages of simpler epitaxial layer structure while maintaining the characteristics of high bandwidth and high output power loss profile. Simulated results show that a large built-in electric field can be generated under illumination which aids UTC carrier velocity. The merits of the new structure are compared to a standard UTC-PD photodiode in terms of improved electric field and carrier concentration response. It is demonstrated that the photogeneration and light absorption of UTC-PD's is improved by incorporating a step-like internal texture of cones and dots profile in the photo absorption layer. The simulated device shows a peak electrical 3-dB bandwidth of 65 GHz at a low light intensity and reverse bias voltage. The performance characteristics of 1D and 2D UTC-PD simulations including internal electric field distribution, energy band diagram, carrier concentration, and power loss distribution, are carefully studied. A theoretical discussion of the working principal and key performance characteristics of a UTC-PD enhanced by heterojunction design are reported. The entire physical environment is modelled and simulated through Finite Element Method (FEM) using commercial software. The proposed photodiode structure configuration is designed and optimised for photodetectors for high RF frequencies at different light intensities. To our knowledge, the obtained bandwidth and electric field response is the fastest among those reported for other various higher wavelength photodiodes.



Index Terms—Uni-traveling Carrier Photodiode, Nano cones, InGaAs, InP, high EM distribution, extreme Electric field, Carrier velocity

I. INTRODUCTION

PHOTODIODES are vital photonic components for many applications, including sensing, radio-over fibre wireless communications methods, high-speed fibre optic systems, and terahertz or millimetre wave generation stratagems etc [1], [2]. A photodiode can possibly operate more quickly than a transistor but on its own it cannot offer electrical amplification

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Turgut Öztürk and Fathi AbdelMalek were as visiting researchers in Department of Electronic Engineering School of Engineering, Physical and Mathematical Sciences Royal Holloway, University of London, Egham, Surrey, TW20 0EX, United Kingdom. Martin Charlton is the Southampton Nanofabrication Center, University of Southampton, University Road, Southampton, SO17 1BJ, United Kingdom. unlike a transistor. In 1997, a new design of photodiode was established; namely the Uni-Traveling-Carrier Photodiode (UTC-PD). The UTC-PD works at much faster modulations and with better linearity. This was achieved by removing the low hole mobility as the rate limiting factor [3]. Practical measurements of the UTC PD confirmed UTC as beneficial to PD operation [4]. Even with the improvements of the UTC photodiodes are a significant constraint in the performance of photonic systems, therefore there is an urgent need to develop higher speed and higher output power photodiodes [3], [4].

Conventional P-I-N Photodiodes (PIN-PD) were used before the invention of UTC-PD; have wide bandwidths – typically up to a 100 GHz [5]-[7]. Both holes and electrons are the active carriers in conventional PIN-PD. However, because holes have a lower mobility than electrons and because electrical neutrality is imposed by the simple PIN design, the higher mobility electrons acquire a drag inflicted by lower mobility holes.

In photodiodes, there is a trade-off between output power and RF bandwidth. This trade-off is caused by a desire to absorb as much light as possible using thick absorption layers whilst



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separating the electrons and holes rapidly through these thicker layers. Various structures have been tried such as depleted absorbers, hybrid absorbers, partially depleted absorbers, dual absorbers in addition to the combination with UTC-PD [8]-[15]. Hybrid absorbers operate through modifying the trade-off relation between PD efficiency and bandwidth by altering electric field and absorption in absorber regions. The absorption and depletion layers are detached in UTC-PD thereby slightly decouple the optical absorption from the RF bandwidth (speed). In this way the UTC-PD EM field response can be increased whilst maintaining speed [10], [14], [15]. Recently, numerous UTC-PD structures have been proposed including stepped or linear doping in absorption layer, improved heterojunctions, and triple transition region structures [16], [17].

Various layer assemblies of InGaAs/InP UTC-PD have been proposed to Increase the electric field in order to separate the carriers rapidly whilst efficiently absorbing high light intensity. In this way overall performance has been improved [4], [5], [8]. Even InGaAs Schottky barrier photodiodes have been utilised but the barrier height of InGaAs is very low (about 0.2 eV to 0.3 eV) and dark current is very high, and so Schottky barriers are of limited use.

In this paper, a new design of UTC photodiode is proposed using inverted cones and circular dots made of Air voids in absorption and collector layers with the aim to increase electric fields and thereby increase carrier velocity. The idea for this research was an extension of our earlier work on textured layers in solar cells boosting their performance [11], [12]. A Full Vectoral Finite Element Method (FVFEM) has been used through the commercial COMSOL Multiphysics simulation software [18]. This idea is a concept and material growers have not been consulted as to its practicality. The modern fabrication methods are very versatile and the structure appears not too outrageous for manufactures to strive towards the design's performance advantages.

In the next sections, the energy band diagrams of a PIN photodiode and UTC photodiode is illustrated. Then the theoretical analysis behind the modelling of UTC-PD and FVFEM simulation model is given. This will then be compared to a reference benchmark performance of UTC-PD structure [19]. Finally, the new design of UTC-PD with the inverted Air cones and circular dots is optimised by varying the spacer/block layer thicknesses. The final structural configuration delivers increased Electric Field (E) in absorber layer of photodiode, improving carrier transportation and higher optical absorption.

The simulation of the photodiode shows electric field distribution through colour representation. The energy band diagram; electron and hole concentration; power loss and power absorbed profile with legends are presented in the next sections.

II. DEVICE DESIGN AND OPERATION

A. Validation

In order to validate the accuracy of our proposed simulation

model and structure configuration, we have compared our simulation results with published research in [19]. Similar results were reproduced. The obtained results are presented in the results and discussion section. To make the simulated results comparable, load resistance is set to 50 Ω , and width 5um is used. The UTC-PD assembly has a thickness of 1.4 μ m including contact layers and excluding the light scattering conical texture structure inside absorber and collector region.

B. Epitaxial Structure

Fig. 1 shows the new proposed UTC-PD design. Table 1 lists epi-layer thickness, material type and doping levels. From top to bottom, the proposed layer structure consists of: a 50nm thick very heavily (1×10^{19}) p-doped InGaAs layer provides the top p-contact, followed by a 20nm thick heavily (1×10^{18}) P doped InGaAsP diffusion block layer. A 300 nm thick heavily P-doped InGaAs layer provides the light absorption layer. A 20nm thick undoped InGaAsP diffusion block layer separates the P-N regions, and is followed by a 30nm thick heavily N-doped InP cliff layer. The underlying collector and N-contact layers are lightly $(2 \times 10^{16} \text{ cm}^{-3})$ and very heavily n-doped InP layers (thickness 300 nm and 800 nm) respectively [20,21].



Fig. 1. Schematics of new UTC-PD structure.

The P contact is very heavily doped $(1 \times 10^{19} \text{ cm}^{-3})$ but just 50nm thick, whereas the N contact is heavily doped $(8 \times 10^{18} \text{ cm}^{-3})$ and very thick (800nm). Since the heavily doped N/P contact layers have no depletion regions they are metallic in nature [22].

This photodiode structure can be grown by epitaxial methods such as Metal - Organic Chemical Vapor Deposition (MOCVD) and Molecular Beam Epitaxy (MBE) [23],[24].

In order to improve the carrier transitions and electric field generation, the following layer properties and doping values were used in Fig. 1 as indicated in Table I and Table II [19].

PROPERTIES OF LAYERS AND MATERIALS FOR PROPOSED UTC-PD CONFIGURATION.							
Layers Properties	P Contact	Absorption	Spacer	Cliff	Collector	N Contact	
Material	InGaAs	InGaAs	InGaAsP	InP	InP	InP	
Thickness (nm)	50	300	20+20	30	300	800	
Band Gap (eV)	0.73	0.73	0.88	1.35	1.35	1.35	
Doping Level (cm ⁻³)	1×10 ¹⁹	1×10^{18}	1×10 ¹⁵	1×10 ¹⁸	2×10 ¹⁶	8×10 ¹⁸	
Dopant Type	Р	P (N:5×10 ¹⁵)	- (N:5×10 ¹⁵)	Ν	Ν	Ν	
Refractive Index (n)	3.42-3.58	3.42-3.58	3.29	3.2-3.167	3.2-3.167	3.2-3.167	
Refractive Index (k)	0.075	0.075	0.0098	0.0	0.0	0.0	
Absorption Coefficient (cm ⁻¹)	10327	10327	941.83	468.72	468.72	468.72	
Dielectric Constant	13.8,	13.8	11.49	12.5	12.5	12.5	
	12.3		10.82	10.11	10.11	10.11	
Electron Mobility	12000 cm ² /Vs	12000 cm ² /Vs	5400 cm ² /Vs	5400 cm ² /Vs	5400 cm ² /Vs	5400 cm ² /Vs	
Hole Mobility	300 cm ² /Vs	300 cm ² /Vs	200 cm ² /Vs	200 cm ² /Vs	200 cm ² /Vs	200 cm ² /Vs	

TABLE I

In this work the addition of inverted cones made of air voids in the absorption layer and circular dots in collector layers play significant role in increasing the electric field and absorption effects. This procedure incorporates air voids through the P-type absorber layer using templated patterning [25,26].

Air voids such as these can be formed by a combination of electron beam lithography, dry etch, and epitaxial overgrowth methods, as has been demonstrated in the recent studies [25,26].

A comprehensive numerical modelling study of the conventional UTC-PD is also provided in recent work [27]. This design of UTC photodiode operates at 1550nm. The bandgap, refractive index and respective material properties are obtained from literature [28],[29] and are also given in Table I and II [19].

C. Electronic Configuration

Under electrically biased illumination conditions, holes generated by light absorption move towards the nearby pcontact, while electrons transit across the depletion region to the collector layer and N-contact.

Since the depletion width in the P region is very small compared to the N region, the p-n junction is said to be onesided.

In a one-sided junction PIN photodiode, light is normally incident onto the bottom N contact layer and passes over the collector layer, which is often a wide band energy gap material (InP). Then the light is absorbed in the absorption layer region, which is composed of low energy gap material (InGaAs). The electron and hole pairs are produced and separated by the electric field in the absorption layer [30], [31]. In the UTC-PD structure, in contrast, there is a delay time due to the minority electrons crossing over the p-type unbiased absorber side, and the traveling distance is equal to the depletion width. The movement of the minority electrons in direct-bandgap III-V materials (for example, InGaAs) is high and quasi-ballistic electron transport appears in the collector, the overall time delay time associated with electron transport can be considerably shortened compared to a PIN structure [30].

TABLE II

MATERIAL PARAMETERS AND PROPERTIES USED IN THE SIMULATION [19]

Parameter	InP	InGaAs
Electron mobility, μ_n	5400 cm ² /Vs	12000 cm ² /Vs
Hole mobility, $\mu_{\rm p}$	200 cm ² /Vs	300 cm ² /Vs
Conduction band density of states, $N_{\rm C}$	$5.7 \times 10^{17} \text{ cm}^{-3}$	$2.1 \times 10^{17} \text{ cm}^{-3}$
Valence band density of states, $N_{\rm V}$	$1.1 \times 10^{19} \text{ cm}^{-3}$	$7.7 \times 10^{18} \text{ cm}^{-3}$
Electron saturation velocity	2.6×10^7 cm/s	2.5×10^7 cm/s
Hole saturation velocity	5×10^6 cm/s	5×10^6 cm/s
Electron and hole life time (UTC-PD)	$1 \times 10^{-9} s$	1×10^{-9} s
Electron and hole life time (OSJ-PD)	2×10^{-9} s	1×10^{-7} s
Electron Auger coefficient	$3.7 \times 10^{-31} \text{ cm}^6/\text{s}$	$3.2 \times 10^{-28} \text{ cm}^6/\text{s}$
Hole Auger coefficient	$8.7 \times 10^{-30} \text{ cm}^6/\text{s}$	$3.2 \times 10^{-28} \text{ cm}^6/\text{s}$
Real refractive index (1550 nm)	3.165	3.595
Imaginary refractive index (1550 nm)	0	0.075
Bandgap E _e	1.35 eV	0.75 eV
Static dielectric constant ε_r	12.5	13.8
Electron saturation velocity vesat	0.85×10^7 cm/s	0.65x10 ⁷ cm/s
Hole saturation velocity vh sat	-	0.48x10 ⁷ cm/s
Electron mobility μ_e	3500 cm ² /Vs	8000 cm ² /Vs
Hole mobility μ_h	150 cm ² /Vs	300 cm ² /Vs
β	7.4×10^{-13}	7.4×10^{-10}
γ	3	2.5
Electron diffusion coefficient D_e	-	200 cm ² /s
Thermal velocity v_{th}	-	5.5x10 ⁷ cm/s

The electron and hole mobilities, carrier saturation velocities, electron-hole life times and their respective band density states are given in Table 1 and II [19], [30]. In Fig. 2, the electrons (black dots) transit much longer distance through the epi-layers to the contact as compared to the holes (yellow dots) which travel shorter distance to the P-contact. The electron/hole transit time can be adjusted by carefully tuning the electric field across the absorption and collector layers.

COMSOL software based on Full Vectoral Finite Element Method (FVFEM) was used to simulate the epi-layer structure and patterning geometry. This is an additional method using economical commercial software enabling initial assessment novel creative ideas. These ideas might then more formally assessed by basic measurements on the device structures compared to the other software and methods.



Fig. 2. Simple schematic Energy Band diagram of UTC Photodiode [30]

This alleviates the effort of the designer whilst maintaining the correct physics, such as Electromagnetic wave frequency domain, Electrostatics, Transport of carriers (and dopants) [18],[32],[33]. The software solves Poisson's transport equation, and the continuity equation for electrons and holes [27], [30] [31]. Time average power loss Q(x, y, z) at nodes in the absorber is considered via electric field distribution by using following equations (1)–(6) [19], [27], [30];

$$\nabla^2 \psi = -\frac{\rho}{\varepsilon} \tag{1}$$

$$\frac{\partial h}{\partial t} = G_n - R_n + \frac{1}{q} \nabla . \vec{J_n}$$
⁽²⁾

$$\frac{\partial p}{\partial t} = G_p - R_p - \frac{1}{q} \nabla . \vec{J_p}$$
(3)

$$\vec{J_n} = q\mu_n n\vec{E} + qD_n \vec{\nabla} n \tag{4}$$

$$J_p = q\mu_p p E - q D_p \nabla p \tag{5}$$

$$Q(x, y, z) = \frac{1}{2} c \varepsilon_0 \eta \alpha |E(x, y, z)|^2$$
(6)

$$f_{3dB} = \frac{1}{\sqrt{\tau_{tr}^2 + \tau_{RC}^2}} \tag{7}$$

Here ψ denotes the electrostatic potential, ε is the permittivity, ρ is the space charge density, J_n and J_p represents the electron and the hole current densities, μ_n and μ_p are electron and hole mobility, D_n and D_p are electron and hole diffusion coefficients, q is electron charge, G_n and G_p are electron and hole generation rates and R_n and R_p are electron and hole recombination rates. In Equation (6), c is the speed of light, ε_0 is free space permittivity, α is the absorption coefficient ($\alpha = 4\pi k/\lambda$) with k being the imaginary part of complex refractive index, η is the real part of complex refractive index, χ is the wavelength and E(x, y, z) is the electric field strength at corresponding excitation wavelength. In equation (7), f_{3dB} represents 3dB bandwidth where τ_{tr} is carrier transition time and τ_{RC} is RC charging time.

The electrical and optical properties of materials used for the simulations including refractive index are taken from literature [27]-[28],[34],[35],[36]. The design parameters and variables are used in the proposed structure simulation are provided in Table 1 and 2. Basic boundary conditions used in the FVFEM simulations are applied to the boundaries/sides of the device and these include: perfect electric conductor, scattering condition, periodic condition, transition boundary condition and surface current density. The model included COMSOL modules of *EM* wave frequency domain, semiconductor model, transport of diluted species, electrostatics, and general Multiphysics compilations. These accounted for the effects of semiconductor optoelectronics interfaces in a simple InP/InGaAs based UTC photodiode structure, and the stimulation of spontaneous emissions in the semiconductor layers. The absorption of the light and the associated change in the electric field distribution are also included in a self-consistent way.

III. RESULTS AND DISCUSSIONS

The electron and hole concentration profiles for reference UTC photodiode and the new proposed UTC PD configuration are illustrated in Fig. 3 and 4. Light is absorbed in the InGaAs absorption region, creating electron hole pairs. The electrons diffuse across the spacer and cliff layers, to the collector layer. A major drawback of this conduction-band offset is space charge build up causes enhanced recombination, leading to a degradation in device bandwidth. Consequently, it is desirable to reduce the effects of the offset.

The high internal electric field is greatly affected by the parameters shown in Table III. Optimisation of these parameters along with the geometrical structures (cones and voids placed in the collector layer) provides a control mechanism. These are fully taken into account in the simulations.

A simple chart in Table III displaying a full investigation of the performance relationship between electric field and other device design parameters, where the upward arrow indicates increasing factor and downward arrow shows decrement. This chart has been designed by taking into account all the simulated parameters to indicate that these parameters have significant effect on Electric field variation. Equation (7) describes the relationship between carrier transit time and 3dB wavelength. We can observe that as the Electric field increase, the 3dB bandwidth of device increases that led to increased carrier transport and decreased Transit time. The output power (OP) of a photodiode can be determined by space charge effect (SCE) and thermal management. The carrier transition time (TS) and charging time can be adjusted independently.

Two optimisations have been applied recently [16],[17]: redesign of spacer layer doping profile, and insertion of a graded layer between absorption and collector layers. Using these two designs approaches the space charge is decreased and the photocurrent loss caused by recombination reduced [16]. Thus, this work presents an approach to utilise modified spacer layer doping profile, a (InGaAsP - 20nm) spacer and cliff layer (InP – 30nm) with suitable doping value of 1 x 10¹⁵ cm⁻³ and 1 x 10¹⁸ cm⁻³ was found to be optimal. The blocking photo-generated electrons transitioning from the absorption layer to collector is mitigated.

In Fig. 3 and 4, the electron concentration across the device is shown at different injected light intensities. The electron concentration for reference UTC photodiode structure is also indicated in the inset to Fig. 3 and for holes in Fig. 4. The hole concentration for new design is maximum at the P contact layer and drops down through absorption and collector layer, where the electron concentration increased at the n contact layer (0.8μ m to 1μ m depth on x-axis frame) and drops at the P contact region for light intensity 1 x 10^4 W/cm² to 8 x 10^5 W/cm². However, the electron concentration in collector layer stops increasing when light intensity goes above 5×10^5 W/cm². At these high light levels, a large number of electrons are collected in the absorber layer and saturation occurs. However, for majority carriers, the holes in the heavily doped p-type absorption layer, returns to equilibrium rapidly through the conduction process. As shown in Fig. 3, the hole concentration across the device only varies slightly on changing the light intensities from 1 x 10^4 W/cm² to 8 x 10^5 W/cm².

TABLE III RELATIONSHIP BETWEEN ELECTRIC FIELD AND DEVICE DESIGN PARAMETERS

$\mathrm{EF}\downarrow$	$\mathrm{CV}\downarrow$	f3-dB BW \downarrow	Performance \downarrow	
Doping ↑	EF ↑	ET ↑	f-dB BW ↑	SCE \downarrow
SCE \uparrow	$\mathrm{EF}\downarrow$	f3-dB BW ↓	$P_{O}\downarrow$	
EF ↑	ET ↑	FR ↑	QE ↑	
EF ↑	f3-dB BW ↑	CT ↑	TS ↓	
$W_C \downarrow$	EF ↑	TS \downarrow		
CL	EF ↑	TS \downarrow	$\mathrm{OP}\uparrow$	f3-dB BW ↑

EF: Electric Field; BW: Bandwidth; ET: Electron Transport; SCE: Space Charge Effects; P_0 : Output Power; FR: Frequency Response; QE: Quantum Efficiency; CT: Carrier Transport (Drift); TS: Transit Time; CV: Carrier (Transit/Drift) Velocity; W_c : Collector Layer Thickness; CL: Cliff Layer; OP: Output Current.

Recently, two significant contactless methods have been utilised practically to limit the semiconductor materials (recombination) lifetime and velocity: the Pump and Probe method (PP) and the Infrared Lifetime Mapping method (ILM), also known as Carriers Density Imaging (CDI) [37]. These methods are used to analyse the free carrier absorption that arises in semiconductor materials. These are based on free carrier absorption contributions present in those material wavelengths with NIR/middle infrared region, corresponding, to the energy lower than the material band-gap. These techniques imply that a distinction of carrier transmittance can be spotted and observed through when a sub-band-gap radiation is transmitted by the material. The PP technique is based on transient methods because they demonstrate a direct analysis to the velocity of the carrier recombination process.

Hence, these are the organized techniques that can be used for this proposed configuration of UTC-PD to determine the concept of carrier recombination lifetime together with the main recombination mechanisms in semiconductor materials.

The key merit to suggest these practical techniques [37] for proposed UTC-PD configuration is the opportunity to directly determine a simultaneous estimation of the bulk recombination carrier lifetime and of the surface recombination velocity. This slight variation of hole concentration in the absorption to collector layer is primarily due to the light absorbed in the generated inverted cones present in absorption InGaAs layer with 1.1um thickness and small circular nanodots of few nano thicknesses (0.1um) in the collector layer. Similar variations of electron concentration in the absorption to collector layer regions are observed at varying light intensity levels. Therefore, the photo response of a UTC-PD configuration is mainly determined by carrier (electron) transportation.



Fig. 3. Hole concentration profiles of proposed UTC-PD structure at different light intensities (different colour lines represent different light intensities starting from 1×10^4 W/cm2 to 8×10^5 W/cm2). The inset is the reference graph of UTC-PD structure with no textures and plain p-absorption-spacer-collector-n contact layer assembly [19].



Fig. 4. Electron concentration profiles of proposed UTC-PD structure at different light intensities (different colour lines represent different light intensities starting from 1×10^4 W/cm2 to 8×10^5 W/cm2). Inset Figure is the reference graph of UTC-PD structure with no textures and plain p-absorption-spacer-collector-n contact layer assembly [19].



Fig. 5. Simulated energy band diagram of UTC-PD for reverse biased voltage of 0V and 4V.

The electron and hole concentration profiles for UTC photodiode across the device are given in Fig. 3 and Fig. 4, respectively. The band energy diagram for UTC structure for applied voltage of 0V and 4V is presented in Fig. 5. The photogenerated electron-hole pairs are separated by internal generated electric field, and electrons and holes move towards adjacent n-type and p-type contact layers respectively.

The photo response of the proposed photodiode is also simulated, including the carrier concentrations, electric fields and hence velocity of travel. The carrier showed low saturation velocity of 5×10^6 cm/s. Unfortunately, at high light intensities, the electrons in the absorption layer are accumulated, consequently caused the electric field to injected light intensity in the collector, leading to further decreasing their speed. Conversely the hole concentration across the device only varies slightly from low 1×10^4 W/cm² to higher light intensities 8×10^5 W/cm² as can be seen in Fig. 3. The hole carriers start to gather near the interface between absorption layer and spacer layer at a light intensity of 8×10^5 W/cm².



Fig. 6. Simulated internal Electric field of reference UTC-PD structure with 4V reverse biased voltage and with no addition of inverted cones and circular dots in absorption and collector layer.

At high light intensity, not only holes but also electrons tend to collect in this region. This is mainly caused by conduction band cut-off between InGaAs and InP. Due to this carrier build up the internal electric field generation tends to decrease. Once the internal electric field drops to below 40 kV/cm, the traveling velocity of holes starts to decrease and holes accumulation occurs. Since there is no valence band discontinuity between p contact layer and absorption layer, the holes can travel easily from absorption layer to the p contact layer.

The energy band diagrams of UTC-PD (Table I) and simulated band energy diagram with reverse biased voltage is illustrated in Fig. 2 and Fig. 5, respectively. The operational mechanism of photodiode structure is given in Fig. 2 [30] where the structure details are described in previous section. In UTC-PD, the electric field in the absorption layer is almost zero. A 20nm thick diffusion block and 30nm thick cliff layers stop photogenerated electrons and holes respectively.



Fig. 7. Simulated internal Electric field of proposed UTC-PD configuration at 10V reverse biased voltage with the addition of inverted cones in absorption layer at incident light intensity 5×10^5 W/cm².



Fig. 8. Simulated Internal field of proposed configuration of UTC-PD at 10V reverse biased voltage with inverted cones and circular dots in

absorption and collector layer at incident light intensity of 8 x $10^5 \ \mbox{W/cm}^2.$

The cliff layer increases electric field and facilitate electron transport at the interface between absorption and collector layers. In this simple one-sided junction photodiode structure, the electric field is so high that electrons can travel through spacer layers easily without a cliff layer. The photo-generated electrons in the p-type absorption layer diffuse or drift into the collection layer, while photogenerated holes are swept out as a conduction current within a time of the same order as the dielectric relaxation time. Consequently, only electrons act as the traveling carriers in a uni-travelling carrier photodiode. Speed is limited by transit time of photogenerated electrons across the absorption and collection layers and can be significantly reduced by adjusting the built-in electric field in the absorption layer which can be done by graded-doping along with introduction of nano cones or oval void structures. The light gets multiply scattered by internal cones and this adds to the optical path length and therefore the light absorption efficiency is also increased. Improved light absorption also creates a high internal electric field, as shown in Fig. 7 and Fig. 8.

Obtained internal electric field results for the reference Photodiode structure are shown in Figure 6. The electric field for the unilluminated reference UTC under 4V reverse Bias voltage conditions and with no addition of inverted cones and circular dots in absorption and collector layer can be seen in Fig. 6. Note, the electric field drops to zero at the interface of absorption layer and therefore electrons start to accumulate. The electric field is minimum in the absorption layer and drops throughout the spacer and collector layers.

The electron concentration and electric field in the collector layer for simulated reference PD structure does not increase with higher incident light intensity.

The simulated internal electric field with 10V reverse bias voltage and injected light intensity 5 x 10^5 W/cm² caused by the addition of inverted cones present in the 1.1um thickness (InGaAs) layer is shown in Fig. 7. The further addition of few 100nm nanodots in the collector layer is shown in Fig. 8. It can be seen that an electric field of magnitude about 3.5×10^5 [V/cm] is generated in the absorption region (at x-axis 0.2-0.3) μm) at maximum light intensity 5 x 10⁵ W/cm². This electric field should be compared to the reference PD structure where the electric field peak is at 5.0 x 10⁴ V/cm. Fig. 8 shows the highest internal electric field peak of 4.0 x 10⁵ [V/cm] at injected light intensity of 5 x 10⁵ W/cm² at 10V reverse biased voltage conditions. This field is generated at the transition from the absorption layer to collector layer. This locally high field leads to increased carrier transport and decrease space charge effects. Notably, the reverse bias voltage has a great influence on the electric field accumulation/ space charge effect, and photocurrent. Photocurrent density usually

because the carriers normally self-screen and not rapidly separated by the field. Thus, it is essential to decrease the offset in conduction band. Electric and magnetic field distributions of the proposed structure are illustrated in Fig. 9 where three different thickness (A,B,C) of internal layer assemblies have been simulated. From these figures field distributions in the internal structure of the diode can be seen.

In the above simulated PD, a uniform doping profile in absorber along with enhanced inner photon scattering is employed. A built-in electric field was generated in the absorption layer by introducing light scattering textures (as shown in Fig. 9) along with layer doping profiles as shown in Table 1. The inverted light scattering cones made of Air voids have a period of between 0.2 um and 2 um. Inverted cones in the absorption (InGaAs) layer with 1.1 um height and small circular nanodots of diameter (0.1um) is integrated. The periodicity in the collector layer is 0.3um in this UTC PD configuration.



Fig. 9. Electric field distribution, magnetic field distribution and power-loss profile in UTC PD configuration under frequency conditions 2.7259E14m and high light intensity 6 x 10⁵ *W/cm2*, where the red colour in colour legends represent highest magnitude of *EM* field and blue color represent minimum levels of field. (A) The thickness of Block layer-10 *nm*, Spacer layer-10 *nm*, Cliff layer 20 *nm*. (B) The thickness of Block layer-15 *nm*, Spacer layer-20 *nm*, Spacer layer-20 *nm*, Cliff layer-30 *nm*

increases with bias voltage. It is worth mentioning that the space charge effect in UTC-PD can be relaxed by modifying uni-traveling carrier photodiode structure.

The conduction-band step between InP and InGaAs can be used to unblock the movement of carriers to the collection layer and thereby prevent their accumulation in the absorption layer. This decrease space charge increases bandwidth It is observed that by increasing of the thickness of both blocking layer and spacer layer, the electric field near the output side of the absorber layer is increased and the electric field in the collector layer drops away, as shown in Fig. 9 (A, B, C). As, the thickness of blocking layer and spacer layer increases from 10nm to 20nm, the electric field near the output side of the absorber region and the electric field in the collector region increases and decreases, respectively. It can be seen in Fig. 9 (C), the electric field and power loss profile of the device changes more rapidly with a 10nm change in thickness of the spacer layer to 20nm and cliff layer thickness of 30nm than when the spacer layer of thickness 10 nm and cliff layer 20nm (Fig. 9(A)). This is due to the fact that the junction capacitance and the electric field of the UTC-PD device is more sensitive to the change of the parameters of spacer/block layers, as shown in Fig. 9.

It is well known that the greatest effect of surface recombination exists for materials with known low surface recombination, such as InGaAs, which has a thin layer relative to its diffusion constant. Similarly, increased recombination occurs when the intensity of light shifts the number of carriers away from equilibrium.

A recent publication proposes a method of passivation of InGaAs columns/pillars (possibly similar to the absorption layer in proposed UTC-PD structure) uses a method to measure the improvement factors [38]. Thus, the published fabricated device has presented the ALD (Atomic Layer Deposition) method for passivation and measured the effects to get comparable results. The manufacturing of the micro/nanopillars was performed using Electron Beam Lithography (EBL) and dry etching processes and the surface passivation treatment comprised a mild wet etching of the active material in an ammonium sulfide, (NH₄)₂S, chemical solution followed by Silicon Oxide, SiO₂, encapsulation by Plasma-Enhanced Chemical Vapor Deposition (PECVD) [38]. This recently published method using InGaAs/InP nanopillars, (with a re-producible, strong, and long-standing passivated surface) can be benchmarked to pre-test and fabricate the nano cones and dots in the proposed UTC-PD layer arrangement.

This method can be used to pre-test the proposed photodiode layers before manufacturing of complete UTC-PD device.

Air voids such as these can be created by a combination of electron beam lithography, dry etch, and epitaxial overgrowth methods, as has been demonstrated in the recent studies [25,26].

In Fig. 9, it can be observed that, the average electric-field strength in the absorber layer and collector layer is increased and decreased, respectively, by varying the thicknesses of spacer layers under light intensity 6×10^5 W/cm². The increased electric field strength in entire region is presented by colour legends. Here, the red and yellow dots represent the maximum generated *EM* field effects (electric field in *V/m*, magnetic field in *A/m*) and accordingly the power loss profile (amount of power absorbed in the absorber layer) is presented by unit W/m³. It is observed that the electric field strength in the absorption layer (the circular red coloured fringes) and collection layer (blue domain) is enhanced to a certain degree by increasing the period of 2 sets of inverted cones placed near the blocking layer side (diffusion block layer-20nm), as shown in Fig. 9.

The conduction band offset and valence band offset increase and decreases, respectively, when the p doping concentration is different between absorption layer and electron barrier layer. This can result in an improved barrier to prevent the diffusion of photogenerated electrons from the absorption layer to the p-contact layer. Meanwhile, such doping enables the photogenerated holes to better reach the pcontact layer. From Fig. 9, it can be seen that the electric-field distribution in the device is more sensitive to the integrated circular dots and inverted cones in the absorption layer and collector layer as well as the thickness of spacer layers.

From the optimisation of the proposed structure, the output power loss profile (W/m^3) of the UTC-PD is improved and more light is captured in the active region. Under high light intensity the photogenerated electrons drift more easily into the collection layer with proposed structure, accordingly increasing the bandwidth of the device.



Fig. 10. Simulations of Bandwidth versus Light intensity for One sided Junction PIN photodiode.

High intensity light causes high concentrations photogenerated electrons and it will lead to decrease the depleted region and the loss of the electric field. Nevertheless, the potential spike becomes larger and the energy band becomes flat.





improved Uni travelling carrier photodiode.

In the simulated UTC photodiode structure, the thickness and doping levels of diffusion block layer, spacer and cliff layer have significant influence on electric field distribution of UTC-PD, as can be seen in Fig. 9.

The bandwidth responses of UTC photodiode and one-sided junction basic photodiode structures are simulated and obtained as shown in Figs. 10 and 11. As a comparison, for both structures, the bandwidth drops because of space charge effect gradually when light intensity increases. Their mechanisms for bandwidth variation spectrum are different. For UTCPD, electrons are driven by concentration rise in the absorption layer. As shown in Fig. 4, electron concentration rise is noticeable when light intensity is below 4×10^5 W/cm² and the gradient varies at light intensities above 5×10^5 W/cm². The distinction of electron concentration incline in Fig. 4 approves the gradient variation of bandwidth in Fig. 11. In case of one-sided photodiode, both electrons and holes are affected by inner electric field. As can be seen in Fig. 10, the internal electric field decrease to zero at high light intensity of 7×10^5 W/cm2. The internal electric field variation in Fig. 6,7 agrees well with the simulation graph of bandwidth in Fig. 10,11. As shown in Figs. 10 and 11, the UTC-PD with 300 nm absorption layer and collector layer has a bandwidth of 34 GHz at low light intensity, while the one sided junction PD bandwidth has a bandwidth of 65 GHz at low light intensity.

In this study, it is observed that with the specific thicknesses of the spacer layer the electric field is strongly confined in absorber regions and become more prominent in those active layers. Thus, from the *EM* field distribution in the proposed configuration can be observed that the photogenerated carriers can simply travel to the contact layers where they can be collected. The built-in electric field dispersal appears uniformly from top to bottom of the device and tends to increase the output efficiency by increasing the concentration of active carriers.

IV. CONCLUSION

A new structure configuration of InP/InGaAs based UTC-PD is designed, analysed and optimised using FVFEM. The concept of UTC-PD is different from the one sided-PD in the epitaxial layer structure, internal electric field distribution, energy band diagram and operation mechanism. The proposed UTC-PD structure with 300 nm absorption layer thickness has achieved a bandwidth of 65 GHz and highest internal electric field peak of 4.0 x 10⁵ [V/cm] under 10V bias voltage. It has been demonstrated that the proposed configuration exhibits major performance characteristics such as; increased electric field for improved speed; increased optical scatter for improved transport of electrons and holes to promote high power handling. The key design performance parameters are optimised enabling the reduction of electron and hole concentration self-screening; electric field distribution (1D and 2D) whereas the thicknesses and materials of block/spacer layers are adjusted to enhance the semiconductor physics. The entire structure is simulated through Finite Element Method (FEM) using commercial

software. The proposed design is verified for fabrication application through recent benchmarked practical techniques (such as Electron Beam Lithography (EBL), dry etching processes and the surface passivation treatment) and would have significant benefits photodetectors for ultra-high-speed state-ofthe-art fibre-optic communication systems, and THz generation with high output power there by help fill the THz gap.

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